

# DIGITAL CLOCK

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## DIGITAL CLOCK

### AIM

To write an assembly language program to generate a digital clock.

### APPARATUS REQUIRED

- 8085 Microprocessor kit
- Power Supply

### ALGORITHM

1. Hours, minutes and seconds are initialized to zero.
2. Increment seconds and decimal adjust it, when it reaches 60, increment a minute counter.
3. Proceed the second step till minute reaches 60. When minute reaches 60, increment a hour counter.
4. Previous two steps are repeated till the clock hour reaches 24.
5. When the hour reaches 24, the entire set up is reset and starts from first step counting the seconds.
6. Continue the execution.

### ASSEMBLY LANGUAGE PROGRAM

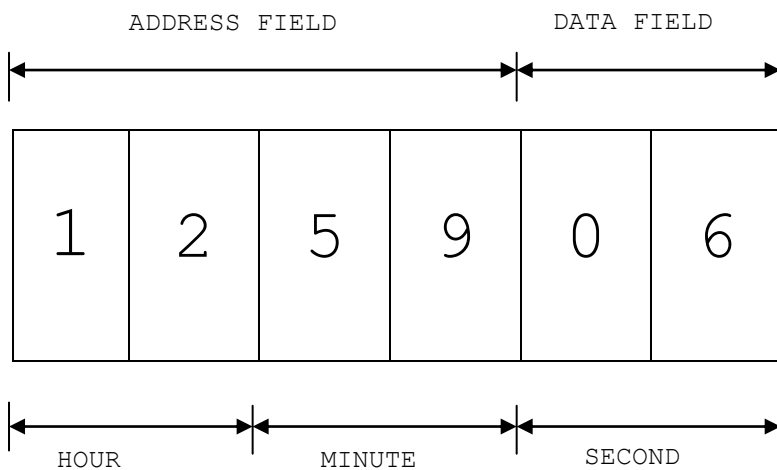
ADDRESS	LABEL	MNEMONICS	OPCODE/OPERAND	COMMENT
C000		LXI SP,C500 <sub>H</sub>	31 00 C5	Initialize the stack pointer for storage purpose
C003		MVI B,24 <sub>H</sub>	06 12	Initialize the hour to be displayed.
C005		MVI C,59 <sub>H</sub>	0E 59	Initialize the minute to be displayed
C007		MOV A,B	78	Store the hour at the address field of the display
C008		STA FFF8 <sub>H</sub>	32 F8 FF	
C00B		PUSH B	C5	Store the hour in stack since while calling the monitor routine the register and flag contents gets destroyed
C00C		CALL UPDAD	CD BC 06	Call the address field memory location using monitor routine
C00F		POP B	C1	Retrieve the stacked hour
C010		MOV A,C	79	Store the minute at the address field of the display
C011		STA FFF7 <sub>H</sub>	32 F7 FF	
C014		PUSH B	C5	Store the minute in stack since while calling the monitor routine the register and flag contents gets destroyed
C015		CALL UPDAD	CD BC 06	Call the address field memory location using monitor routine

C018		<b>POP B</b>	C1	Retrieve the stacked minute
C019		<b>JMP HOUR</b>	C3 07 C1	Jump to Label HOUR, to start the digital count
C100	<b>RESET</b>	<b>LXI SP,C500<sub>H</sub></b>	31 00 C5	When the 24 hours are over, the entire set up is reset i.e all the counters are initialized to 00 <sub>H</sub> , to start afresh
C103		<b>MVI B,00<sub>H</sub></b>	06 00	
C105		<b>MVI C,00<sub>H</sub></b>	0E 00	
C107	<b>HOUR</b>	<b>MVI A,00<sub>H</sub></b>	3E 00	
C109	<b>MIN</b>	<b>STA FFF9<sub>H</sub></b>	32 F9 FF	
C10C		<b>PUSH PSW</b>	F5	Accumulator content is stored as the second at the data field of the display for each looping
C10D		<b>PUSH B</b>	C5	
C10E		<b>CALL UPDDT</b>	CD D3 06	
C111		<b>CALL DELAY</b>	CD 00 C2	
C114		<b>POP B</b>	C1	
C115		<b>POP PSW</b>	F1	
C116		<b>ADI 01<sub>H</sub></b>	C6 01	
C118		<b>DAA</b>	27	The 8 bit number in the accumulator is adjusted to form two four bit BCD digits. This uses the auxiliary flag (internally) to perform the binary to BCD conversion. DAA is used, in order to avoid displaying of the hexcode in the digital clock.
C119		<b>CPI 60<sub>H</sub></b>	FE 60	Compare whether the second has reached 60.
C11B		<b>JNZ MIN</b>	C2 09 C1	When 60 seconds are over ,then minute counter is incremented by one.
C11E		<b>MOV A,C</b>	79	Minute is moved to accumulator
C11F		<b>ADI 01<sub>H</sub></b>	C6 01	Increment the minute
C121		<b>DAA</b>	27	Perform the binary to BCD conversion.
C122		<b>MOV C,A</b>	4F	Minute is stored in C register
C123		<b>STA FFF7<sub>H</sub></b>	32 F7 FF	Accumulator content is stored as the minute at the address field of the display for each looping
C126		<b>PUSH B</b>	C5	To display minute, call the address field of display using monitor routine, by stacking the hour content present in B register
C127		<b>CALL UPDAD</b>	CD BC 06	
C12A		<b>POP B</b>	C1	
C12B		<b>MOV A,C</b>	79	Minute is moved to

				accumulator
C12C		<b>CPI 60<sub>H</sub></b>	FE 60	Compare whether the minute has reached 60.
C12E		<b>JNZ HOUR</b>	C2 07 C1	When 60 minutes are over ,then hour counter is incremented by one.
C131		<b>MVI C,00<sub>H</sub></b>	0E 00	In order to reset the minute counter, C register is initialized to 00 <sub>H</sub> .
C133		<b>MOV A,C</b>	79	Minute is moved to accumulator
C134		<b>STA FFF7<sub>H</sub></b>	32 F7 FF	Accumulator content is stored as the minute at the address field of the display for each looping
C137		<b>PUSH B</b>	C5	To display minute, call the address field of display using monitor routine, by stacking the hour content present in B register
C138		<b>CALL UPDAD</b>	CD BC 06	
C13B		<b>POP B</b>	C1	
C13C		<b>MOV A,B</b>	78	Hour is moved to accumulator
C13D		<b>ADI 01<sub>H</sub></b>	C6 01	Increment the hour
C13F		<b>DAA</b>	27	Perform the binary to BCD conversion.
C140		<b>MOV B,A</b>	47	Hour is moved to B register
C141		<b>STA FFF8<sub>H</sub></b>	32 F8 FF	Accumulator content is stored as the hour at the address field of the display for each looping
C144		<b>PUSH B</b>	C5	Stack the hour & display it by calling the address field of the display using monitor routine.
C145		<b>CALL UPDAD</b>	CD BC 06	
C148		<b>POP B</b>	C1	
C149		<b>MOV A,B</b>	78	Hour is moved to accumulator
C14A		<b>CPI 25<sub>H</sub></b>	FE 13	Compare whether the 24 hours are completed.
C14C		<b>JNZ HOUR</b>	C2 07 C1	When 24 hours are not over ,then hour counter is incremented by one.
C14F		<b>MOV B,01<sub>H</sub></b>	06 01	Reinitialize the hour
C151		<b>MOV A,B</b>	78	Hour is moved to accumulator
C152		<b>STA FFF8<sub>H</sub></b>	32 F8 FF	Accumulator content is stored as the hour at the address field of the display for each looping
C155		<b>PUSH B</b>	C5	Stack the hour and display it by calling the address field of the display using monitor routine.
C156		<b>CALL UPDAD</b>	CD BC 06	
C159		<b>POP B</b>	C1	
C15A		<b>JMP RESET</b>	C3 00 C1	Jump to label RESET.
C200		<b>MVI B,02<sub>H</sub></b>	C6 02	The delay time is actually

C202	<b>DELAY</b>	<b>LXI D,A000<sub>H</sub></b>	11 00 A0	the calculation of the total execution time for the program.  Delay subprogram is used to generate the frequency which is inversely proportional to time period. In order to make the visibility of the digits in the display.
C205	<b>X1</b>	<b>DCX D</b>	1B	
C206		<b>MOV A,E</b>	7B	
C207		<b>ORA D</b>	B2	
C208		<b>JNZ X1</b>	C2 05 C2	
C20B		<b>DCR B</b>	05	
C20C		<b>JNZ DELAY</b>	C2 02 C2	
C20F		<b>RET</b>	C9	

**EXECUTION**



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